

Fig. 1
(Prior Art)

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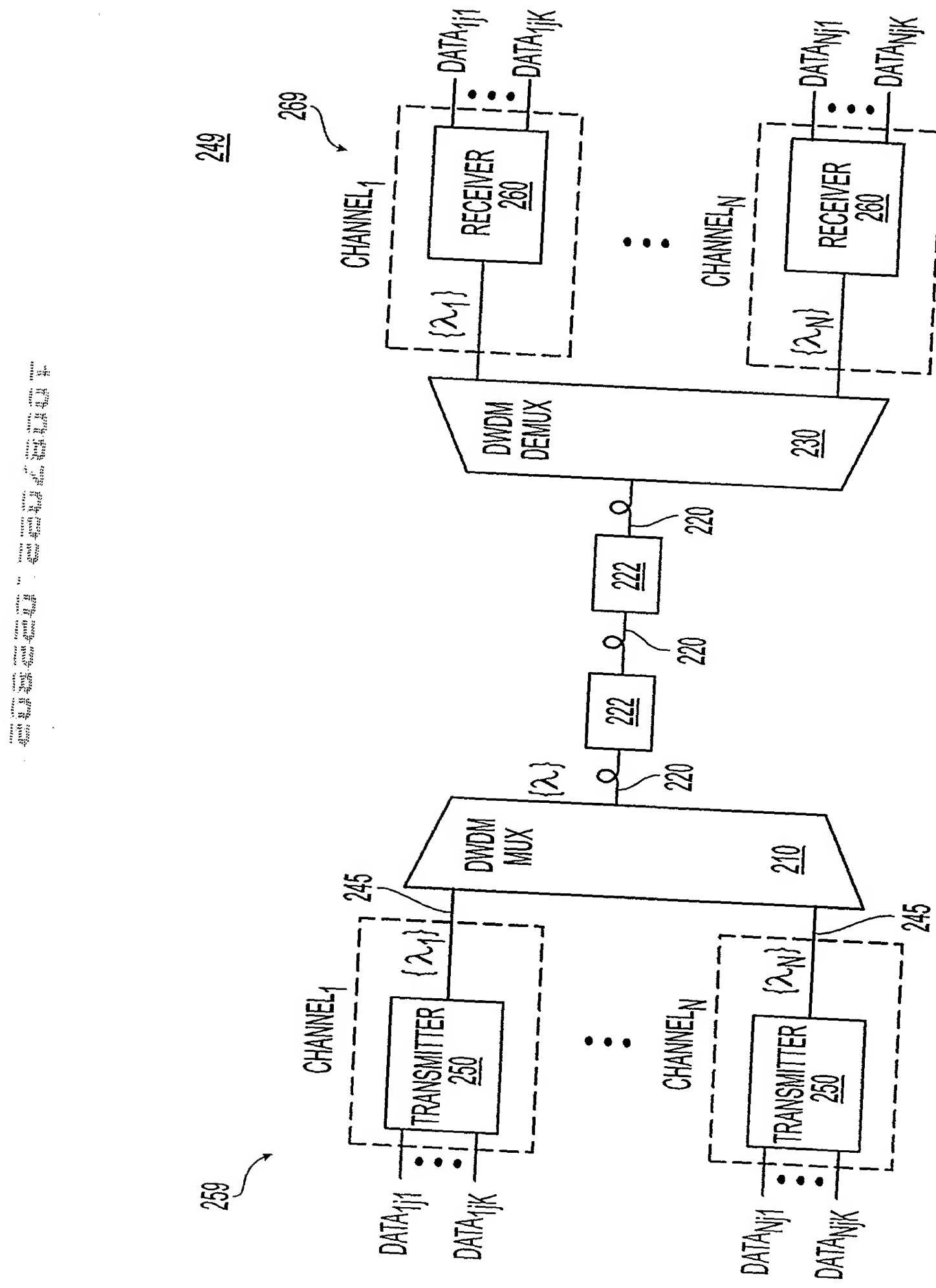


Fig. 2a

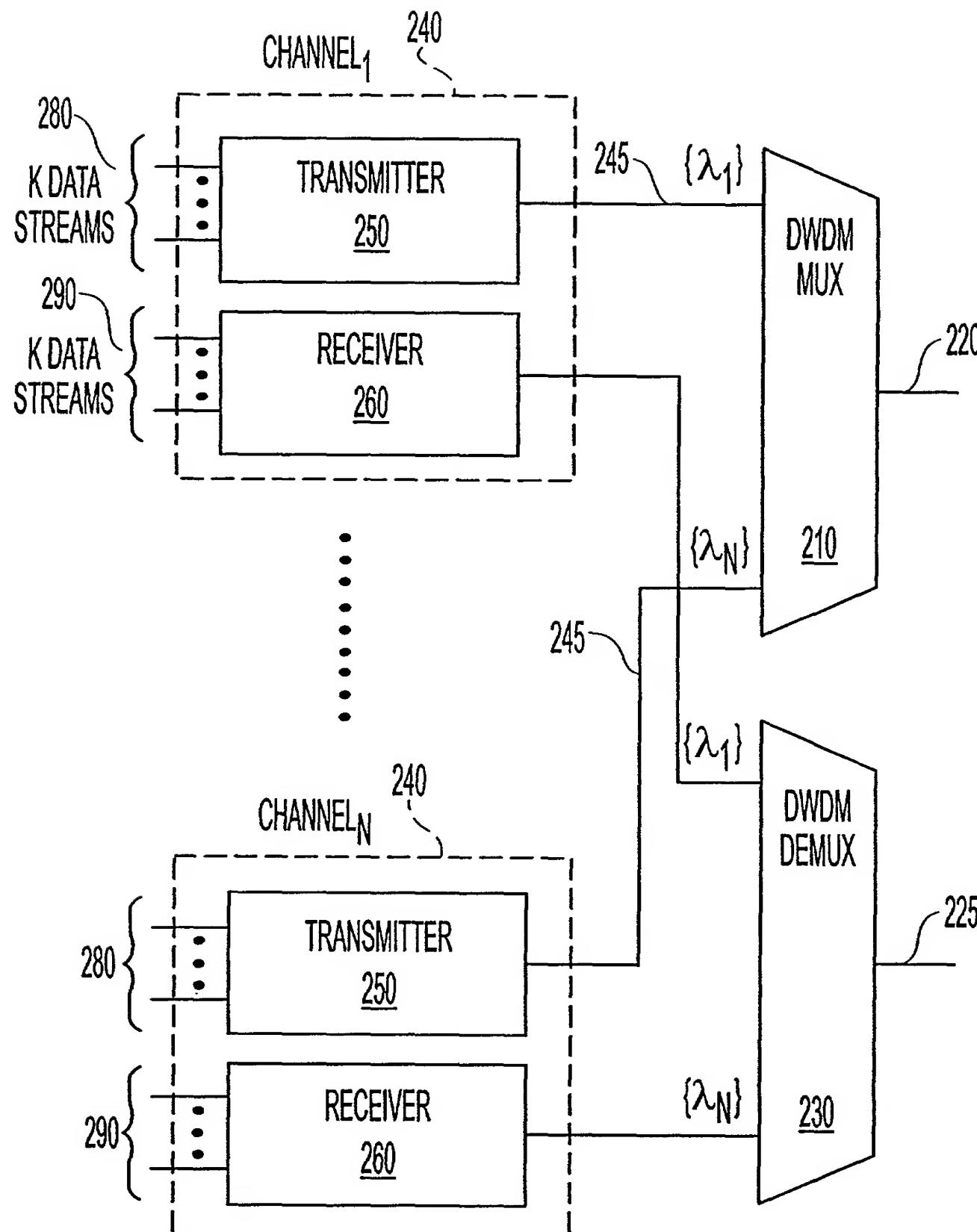


Fig. 2b

TRANSMITTER FOR CHANNEL i

250

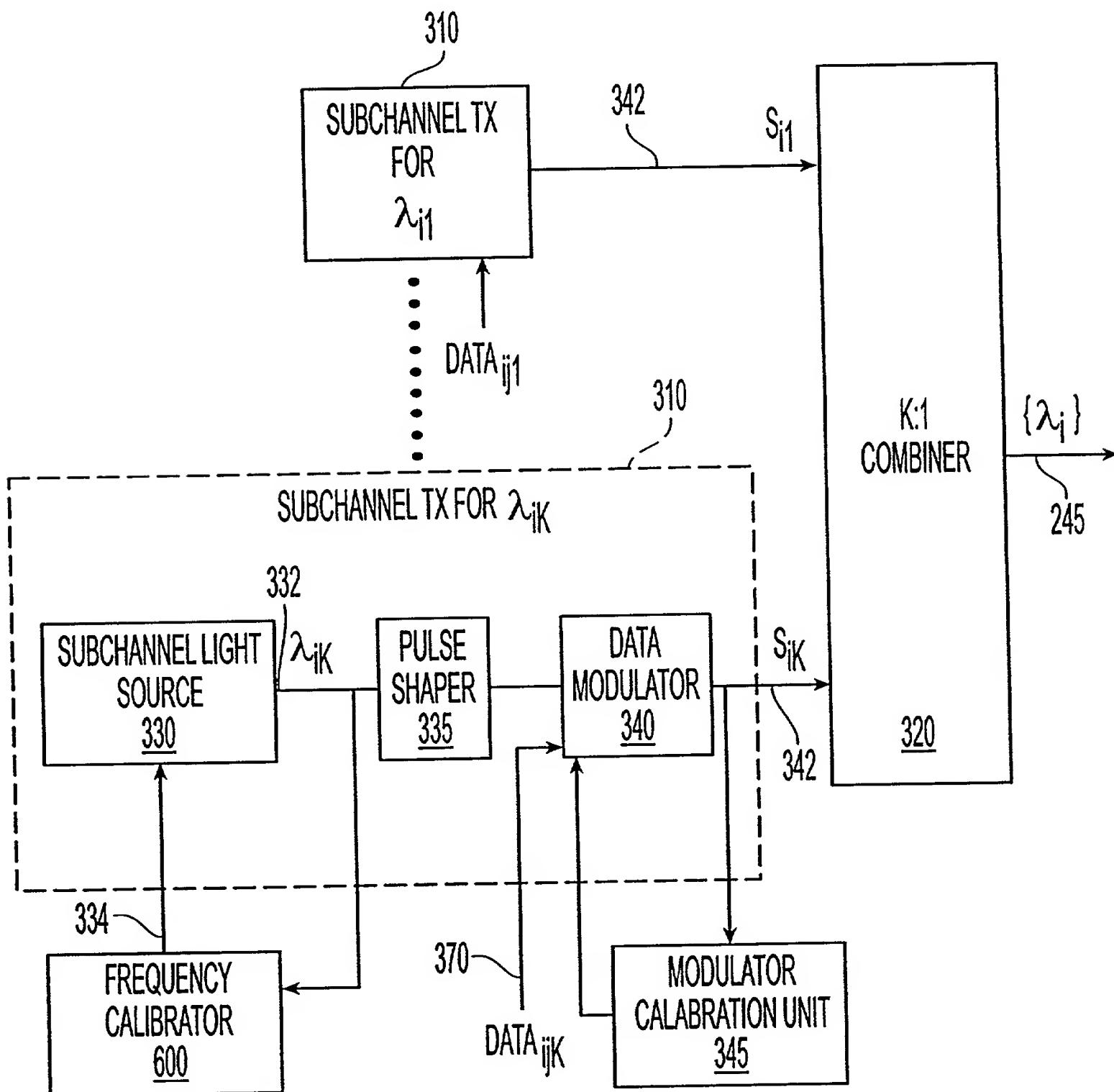


Fig. 3a

TRANSMITTER FOR CHANNEL i

1400

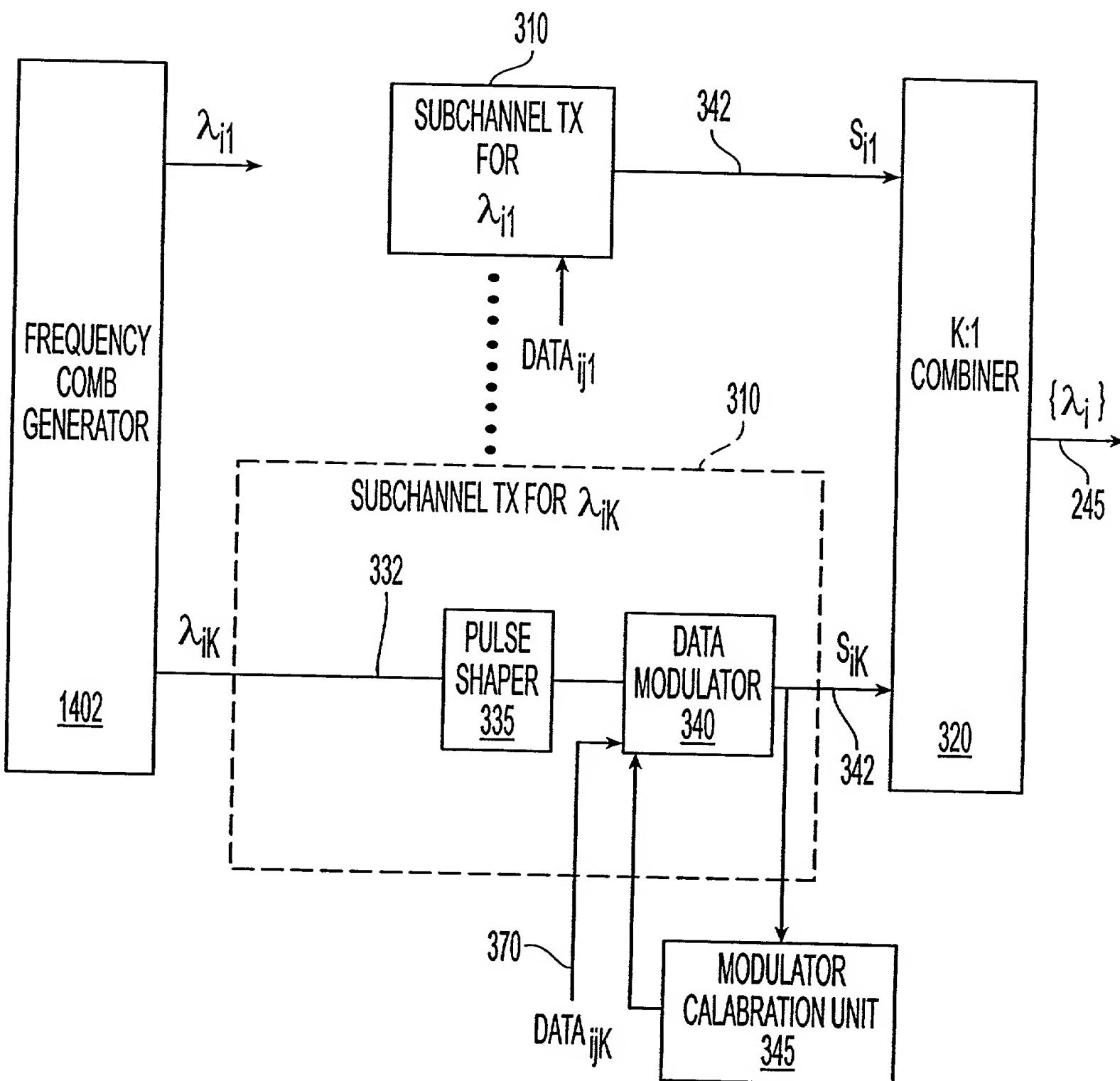


Fig. 3b

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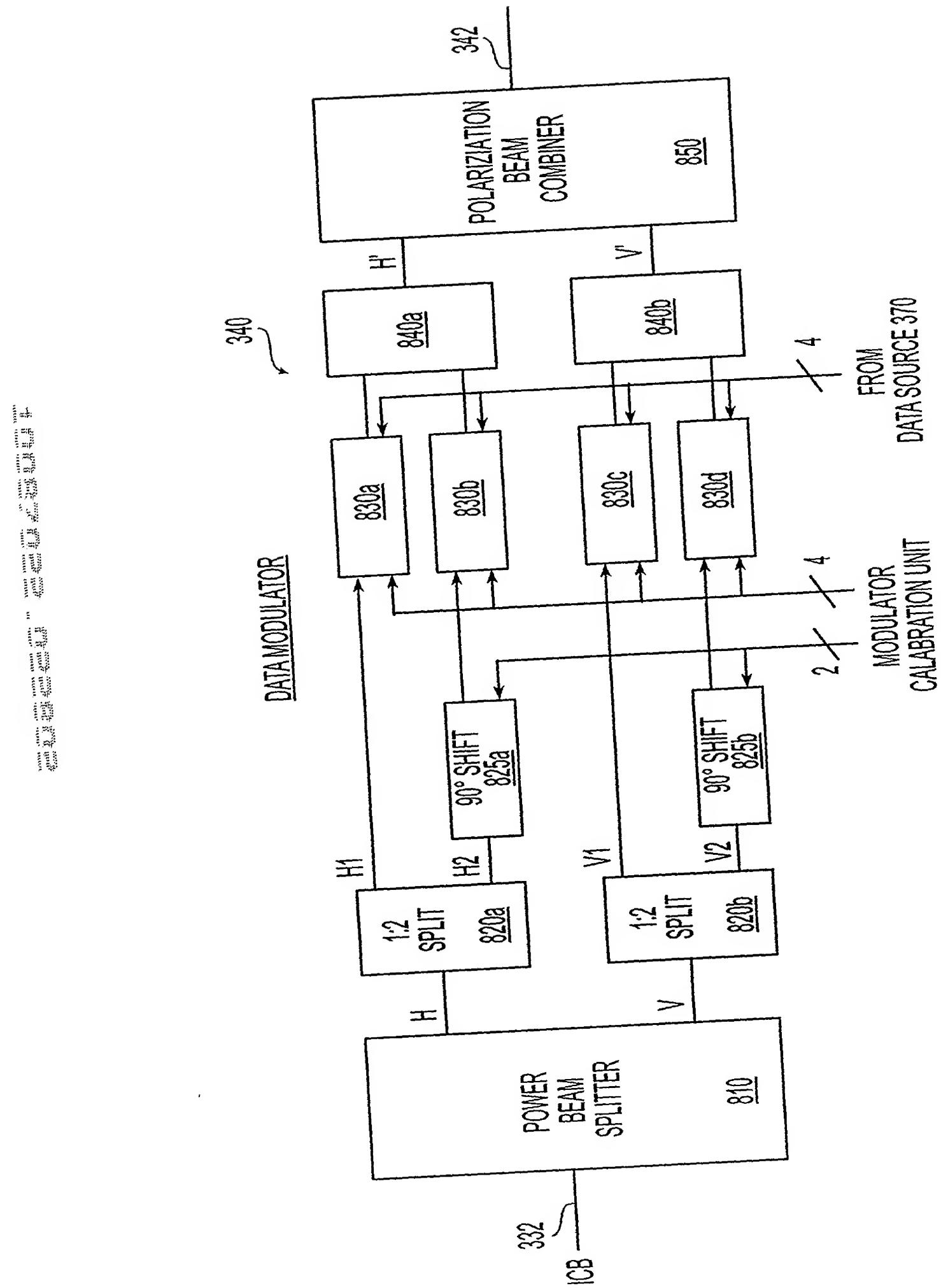


Fig. 4

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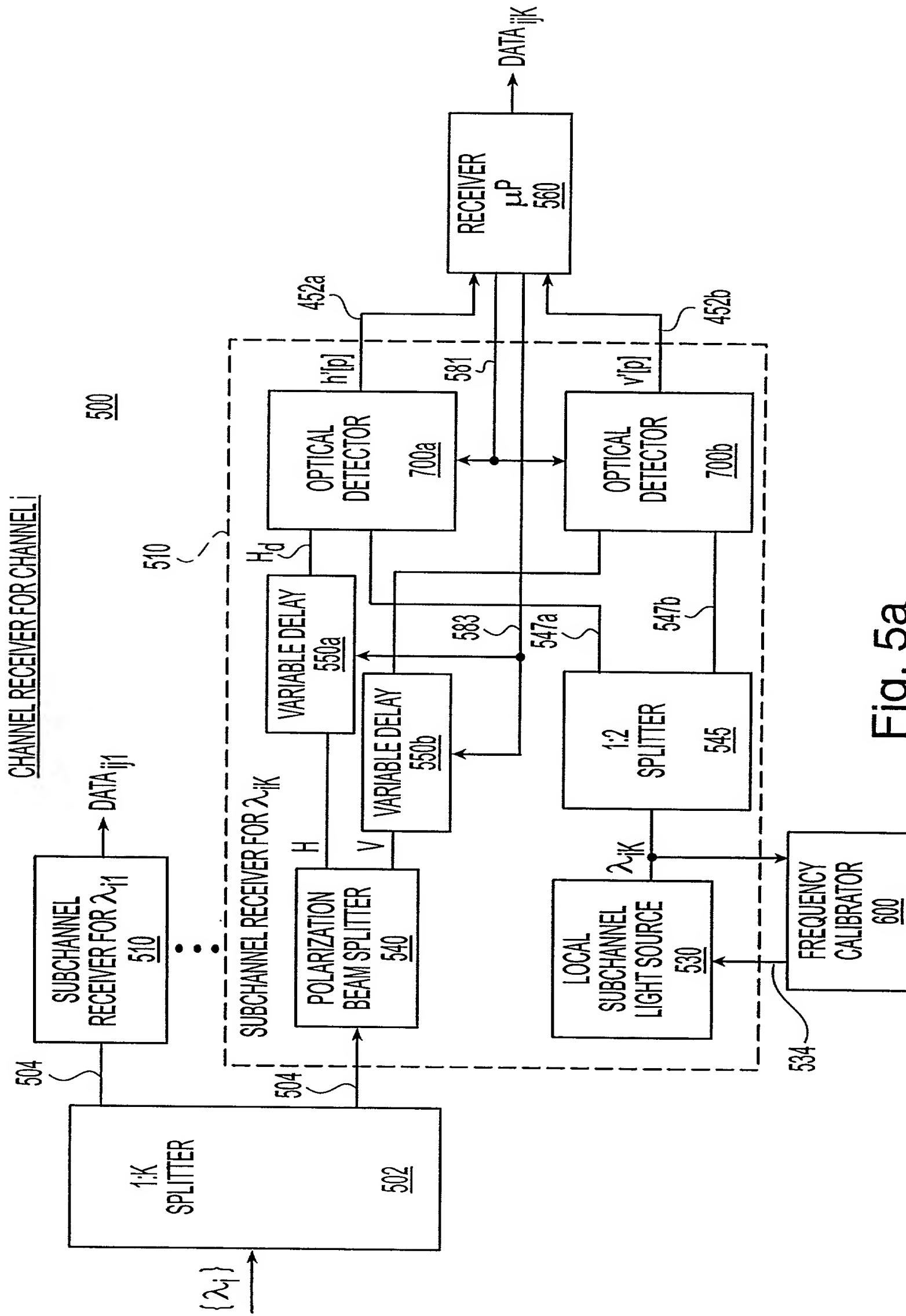


Fig. 5a

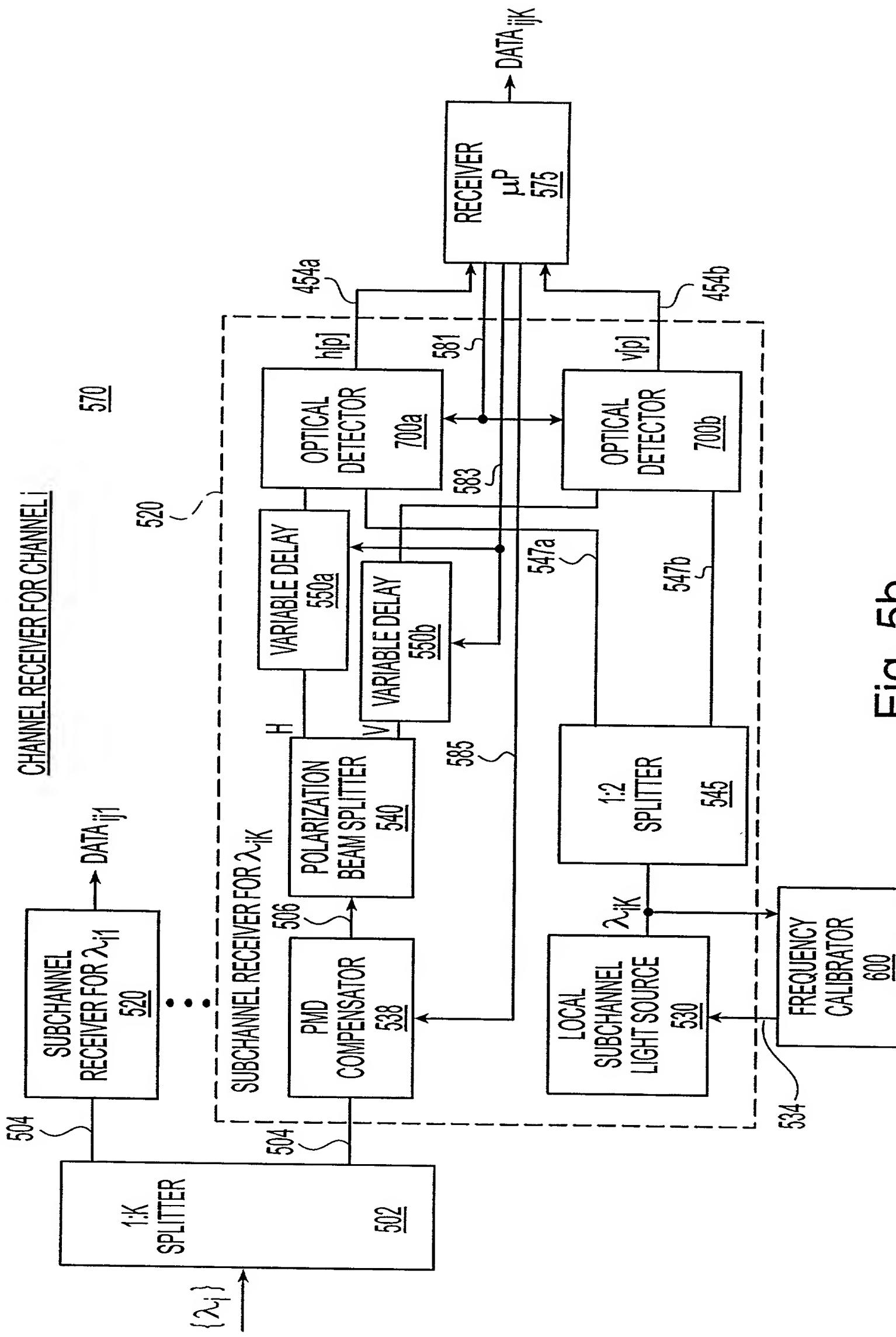


Fig. 5b

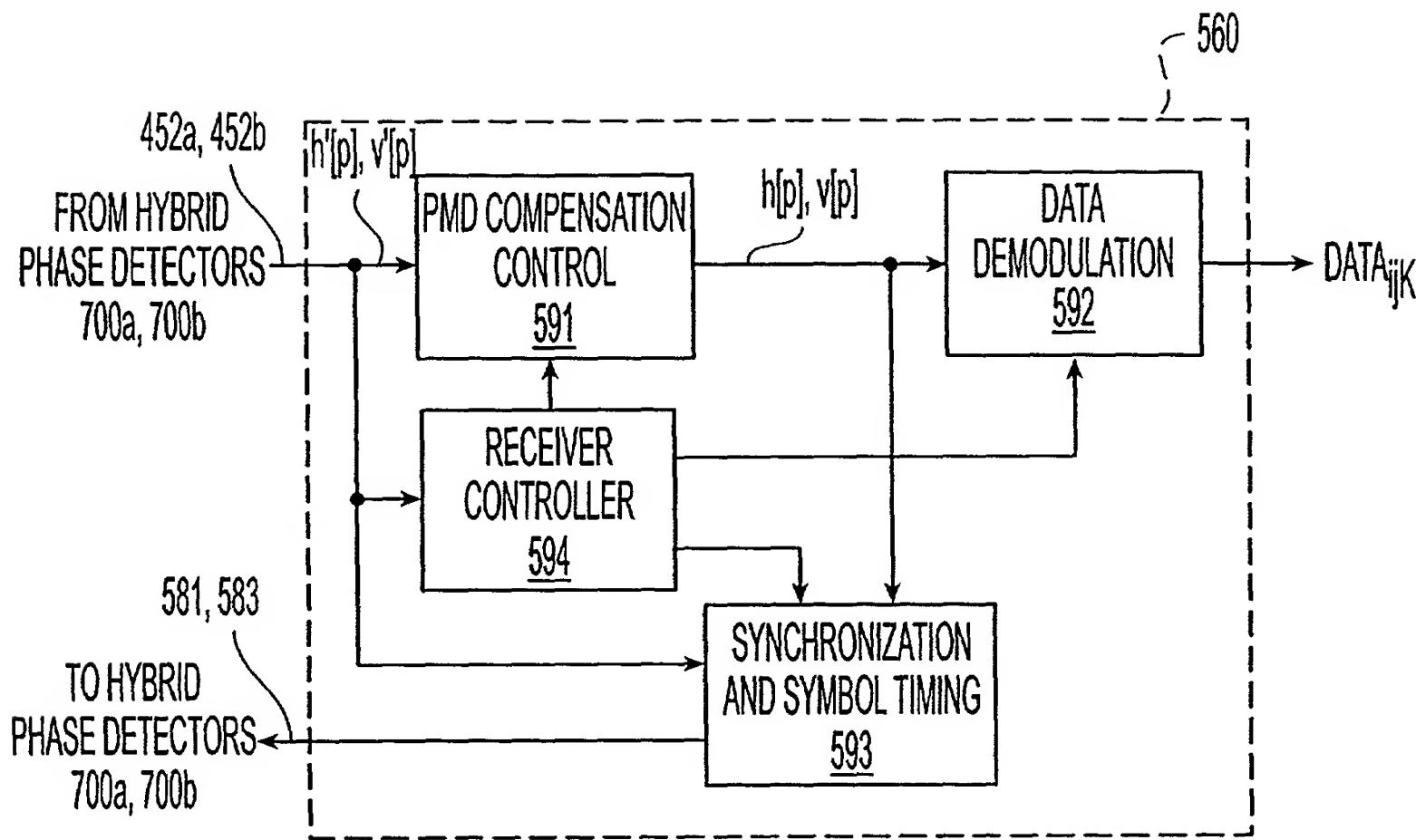


Fig. 5c

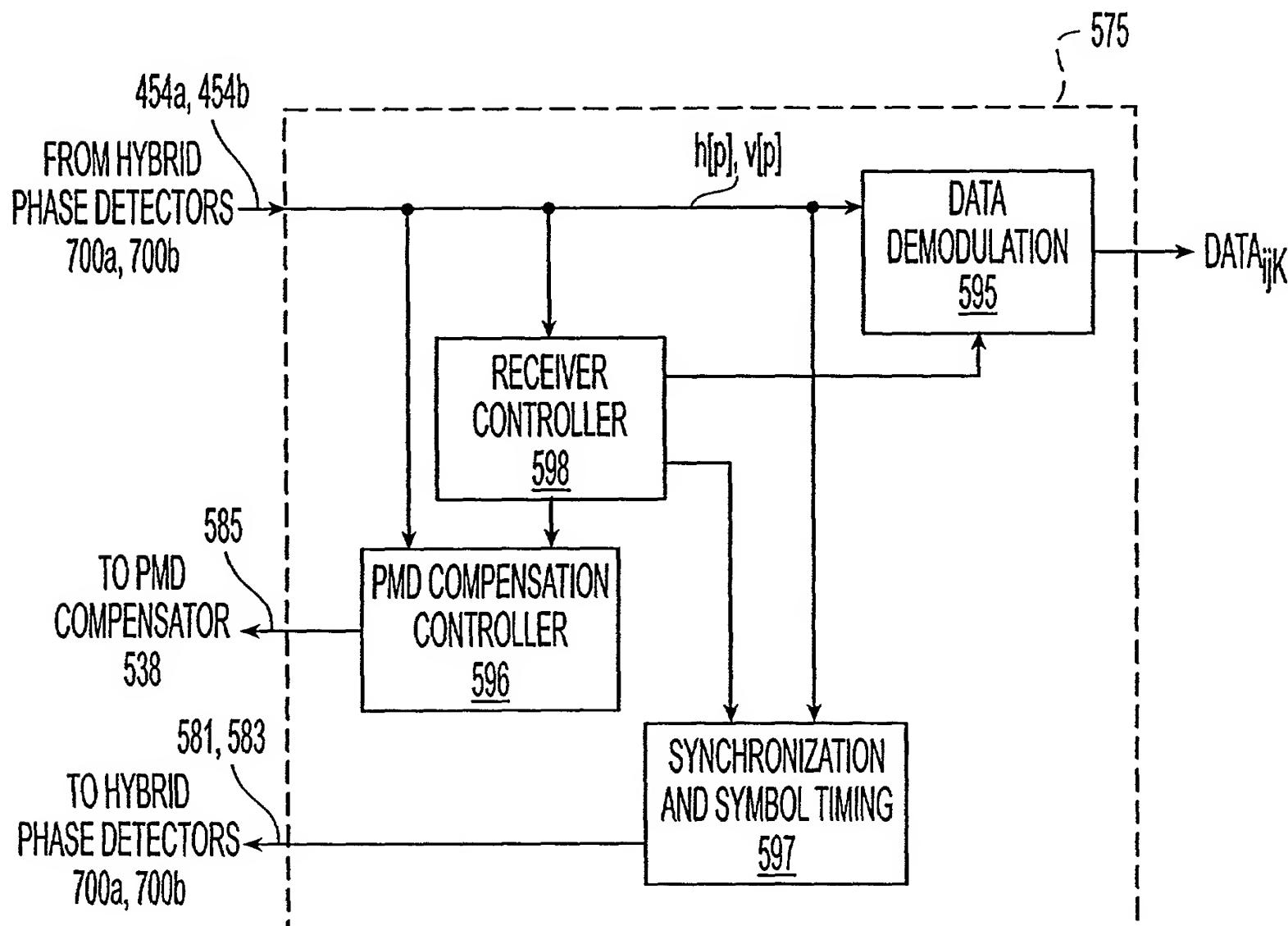


Fig. 5d

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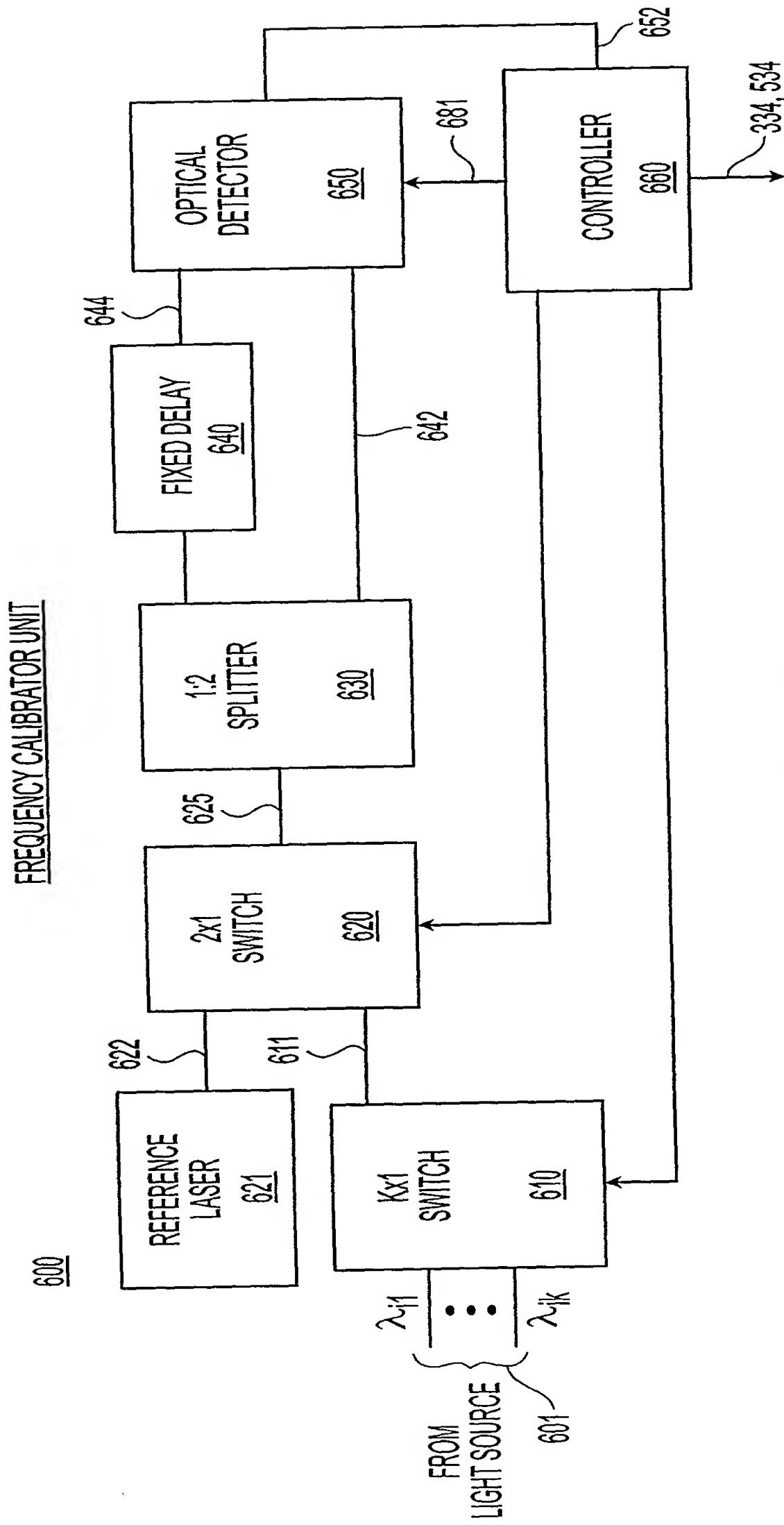


Fig. 6

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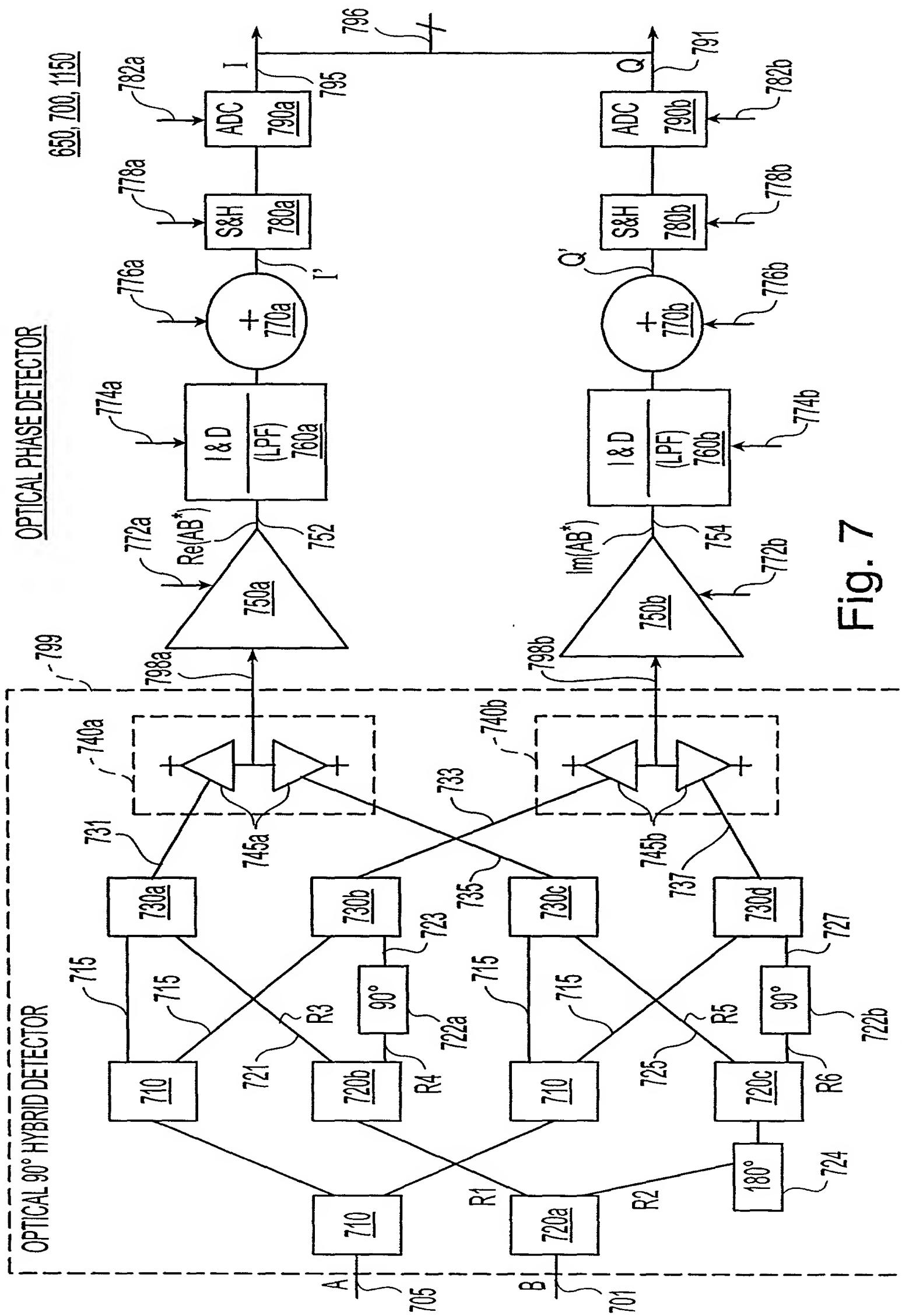


Fig. 7

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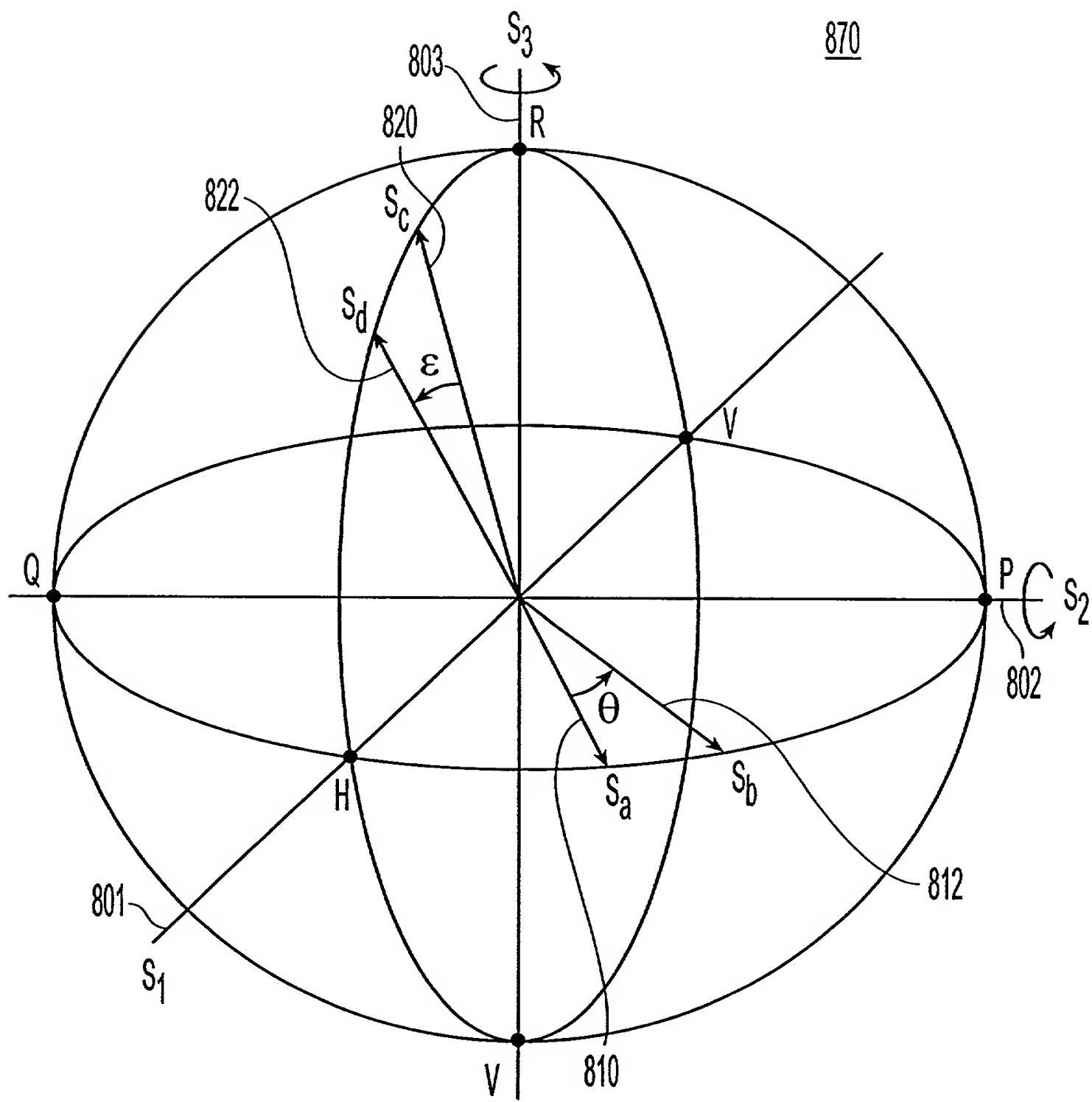


Fig. 8

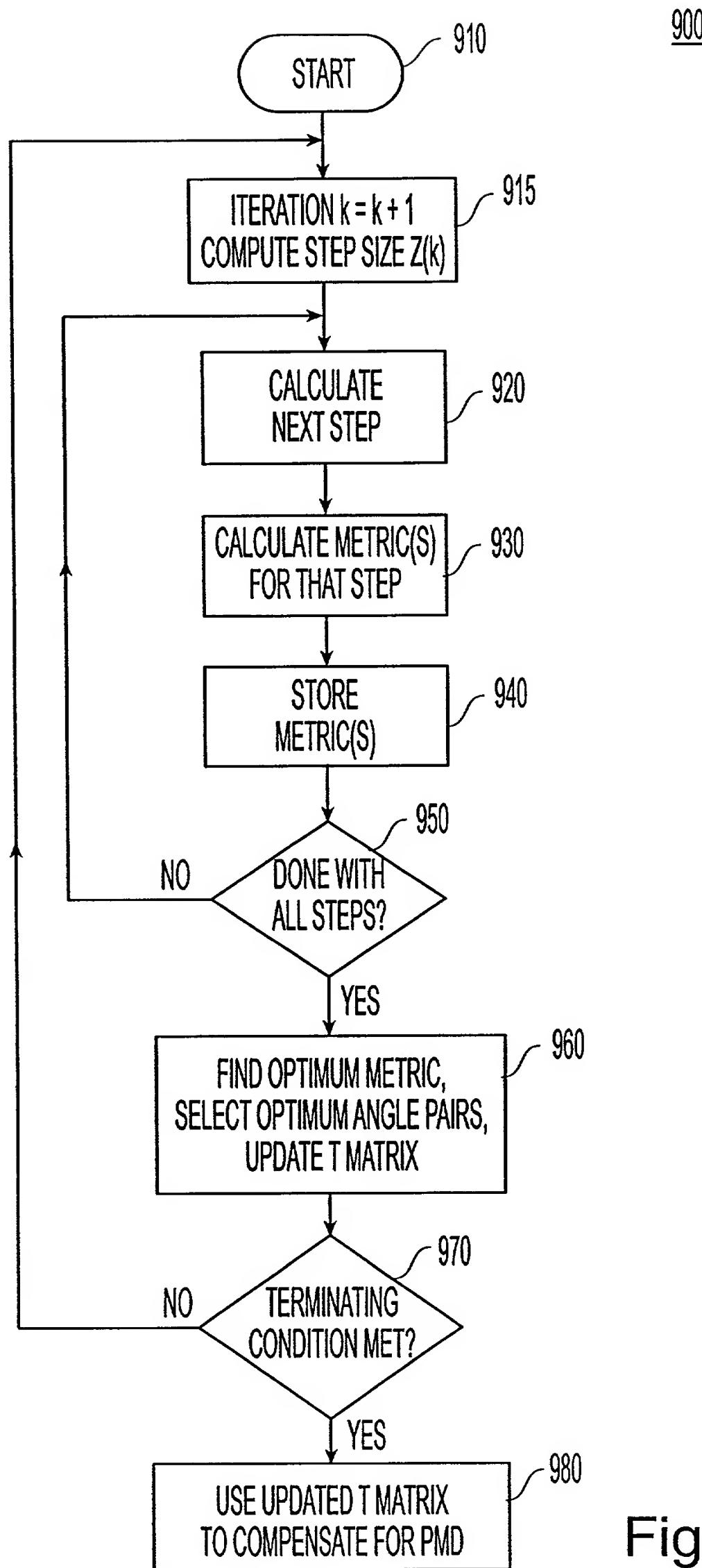


Fig. 9a

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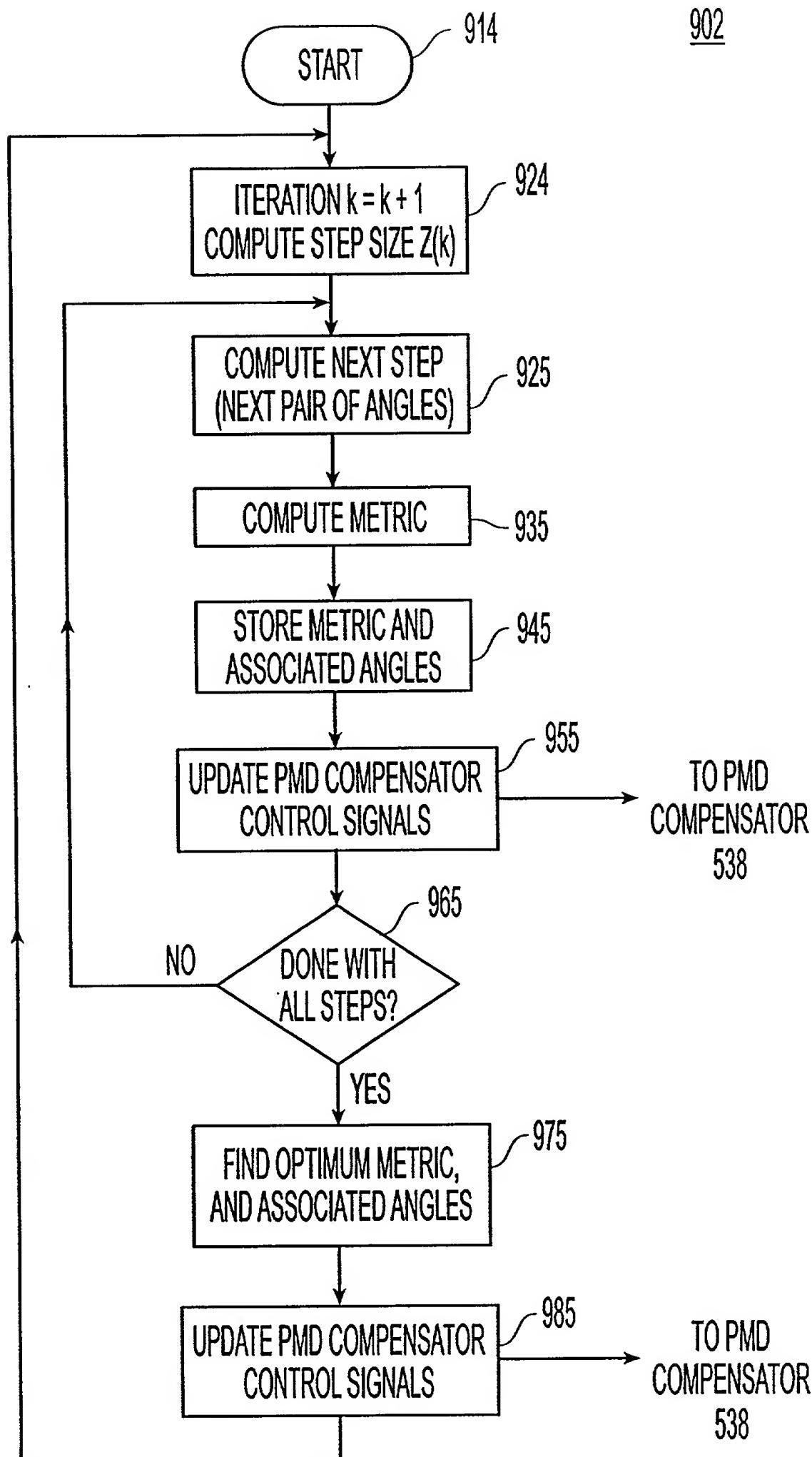


Fig. 9b

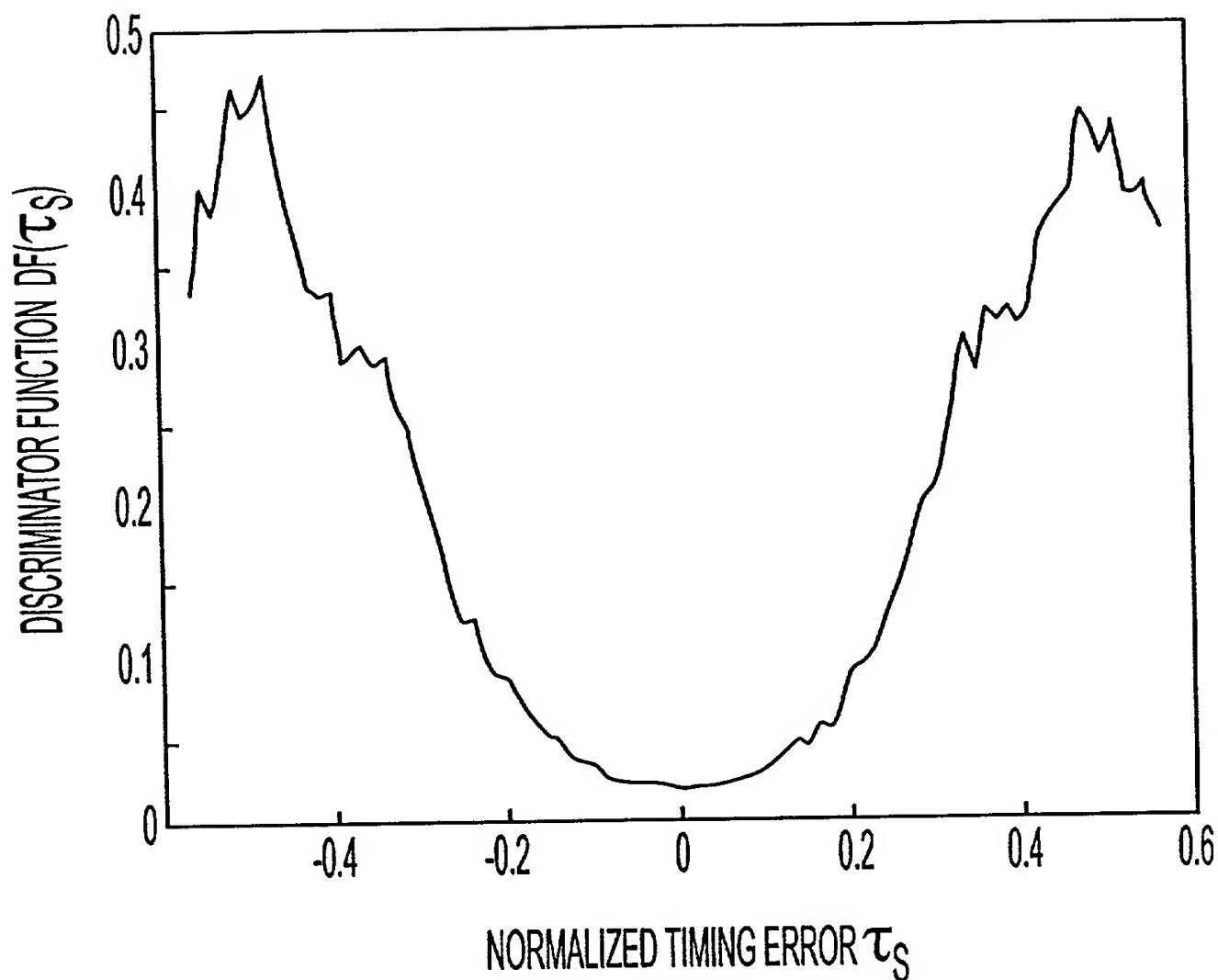


Fig. 10

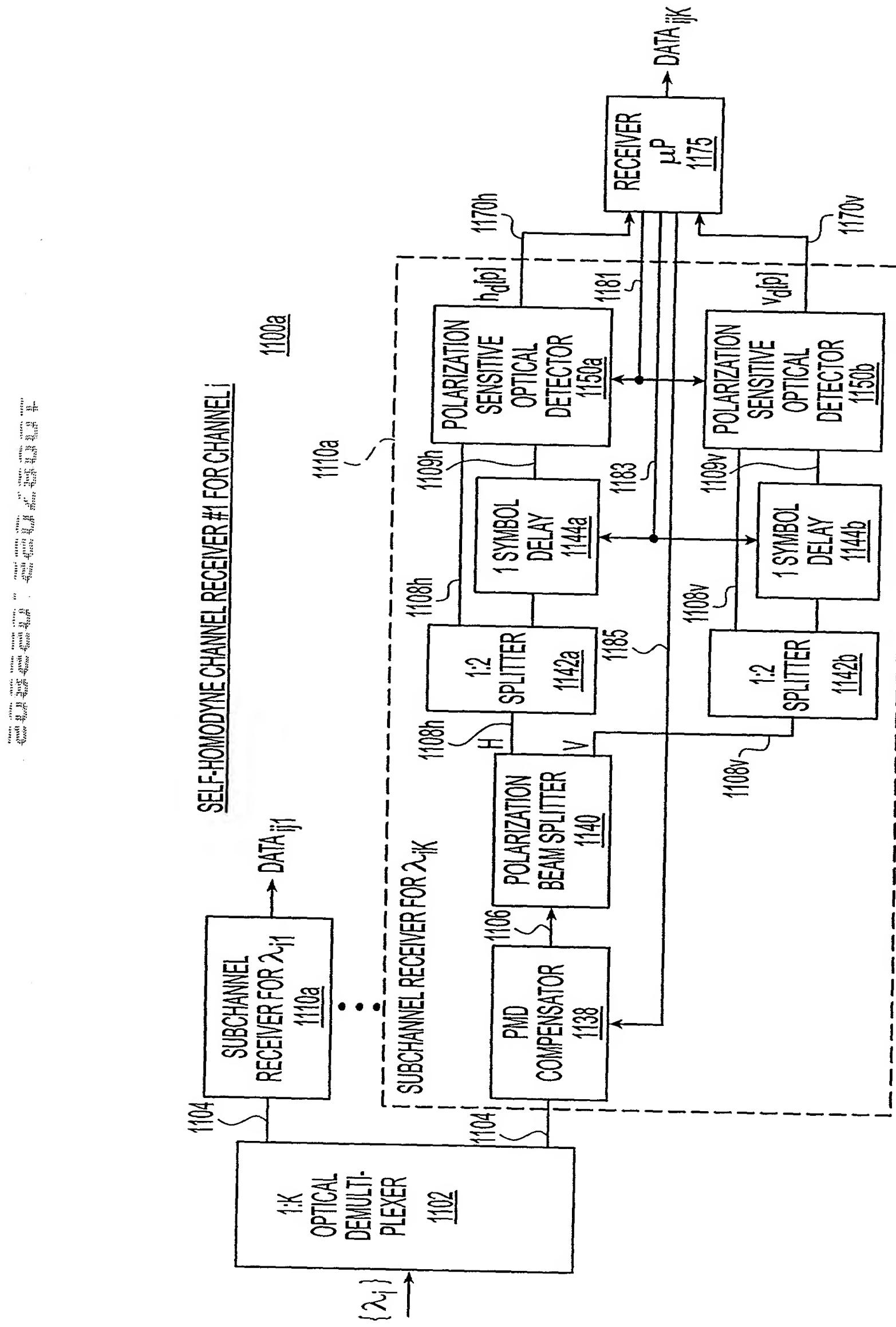


Fig. 11a

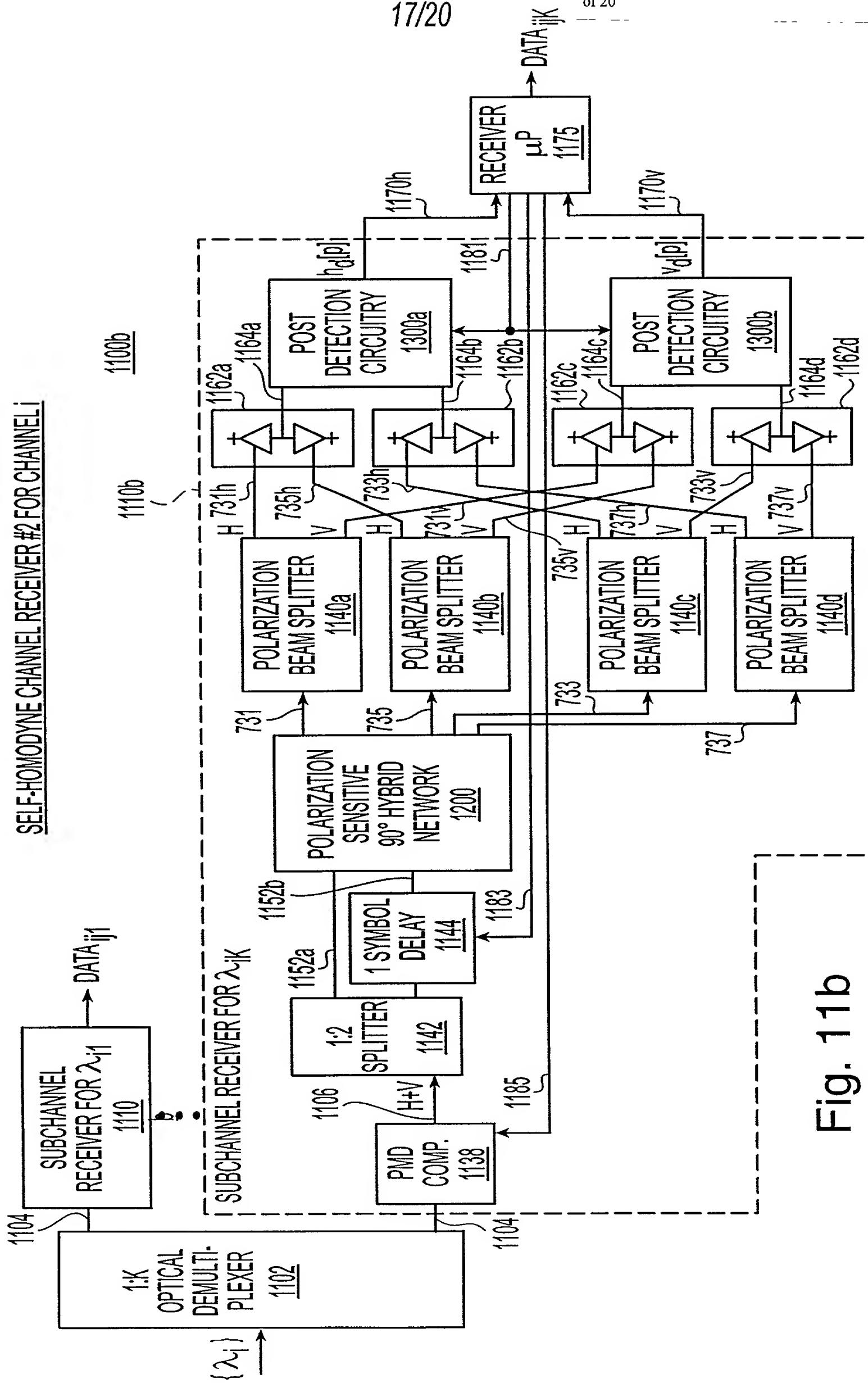


Fig. 11b

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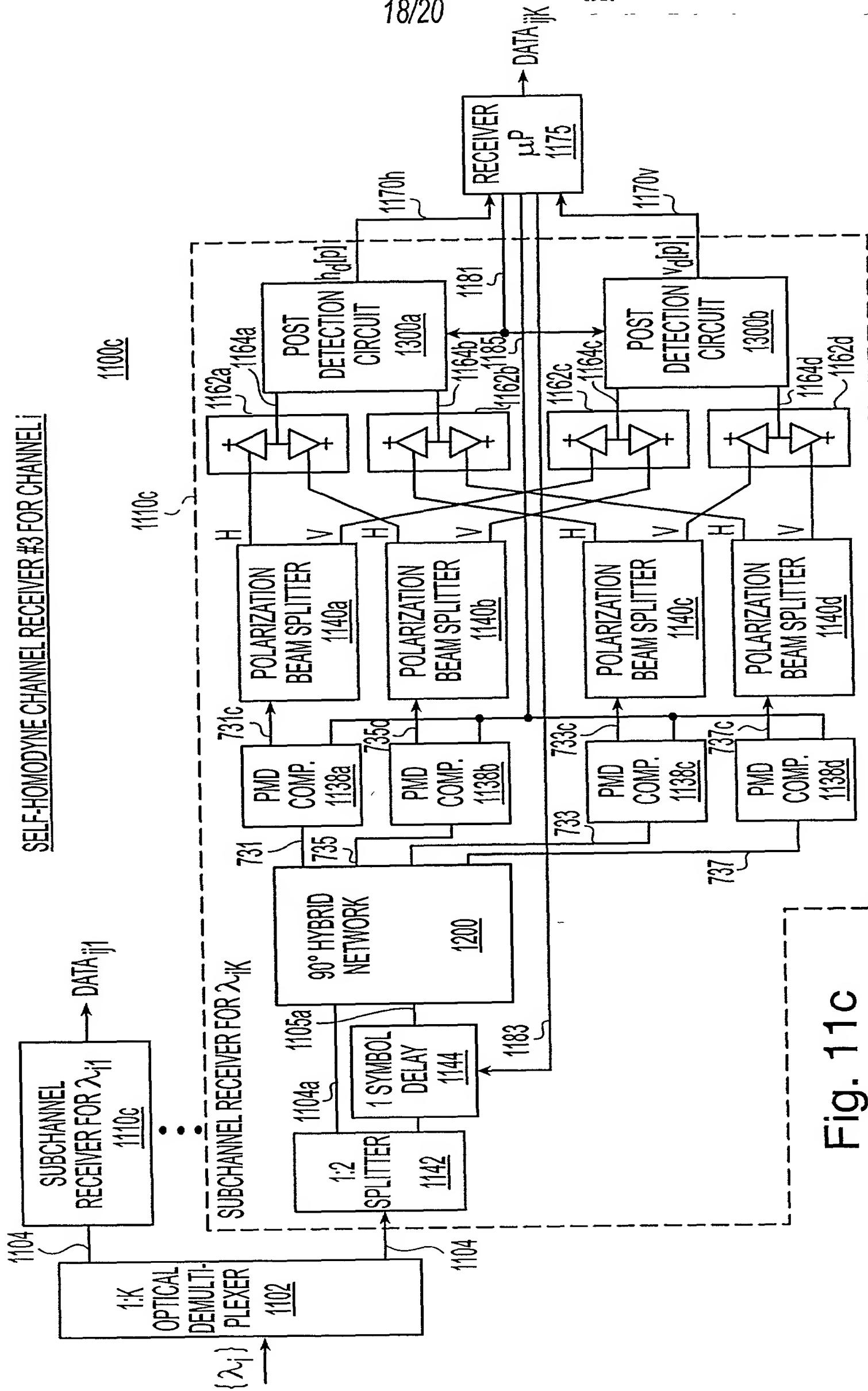


Fig. 11C

1200

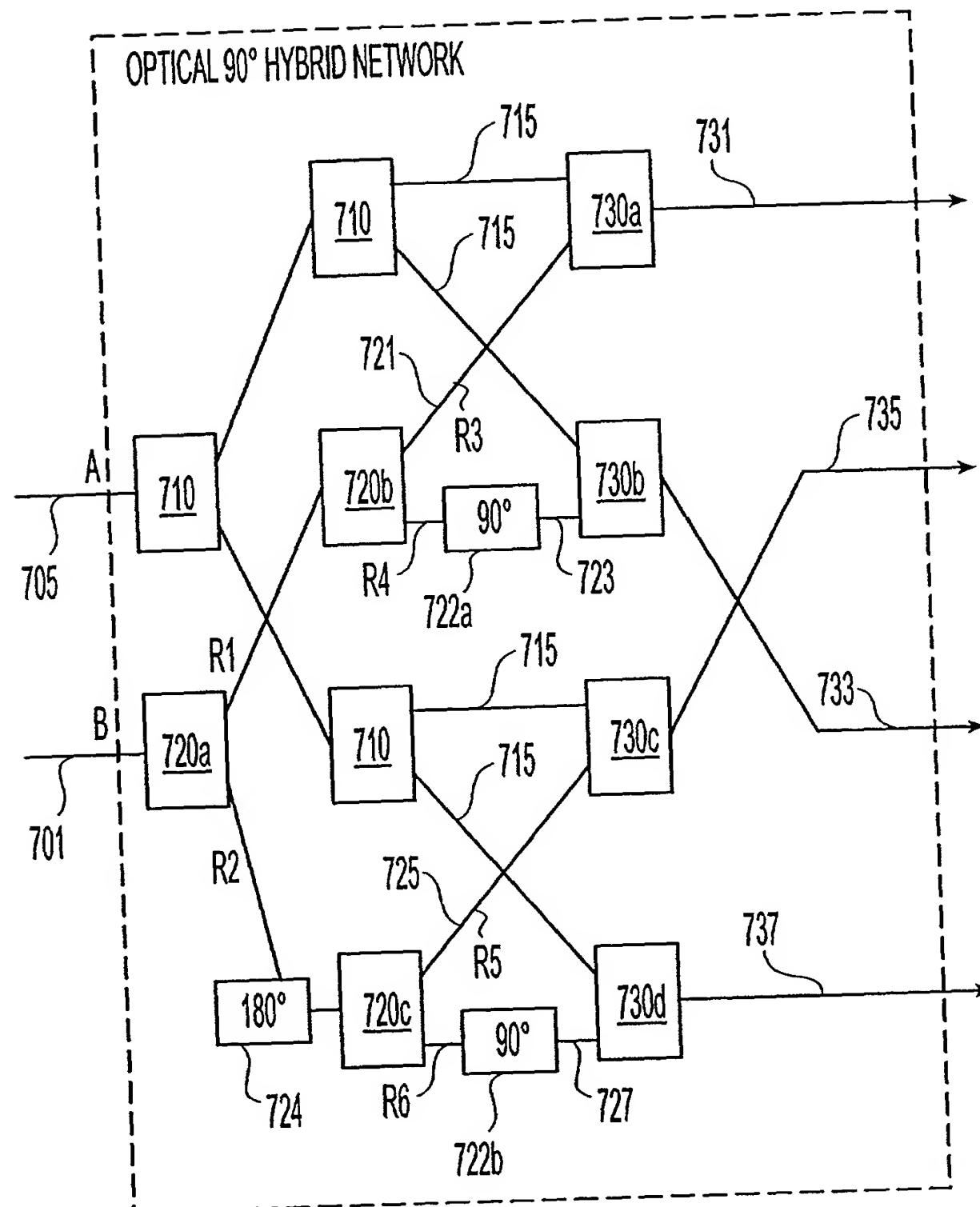


Fig. 12

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1300

POST DETECTION CIRCUIT

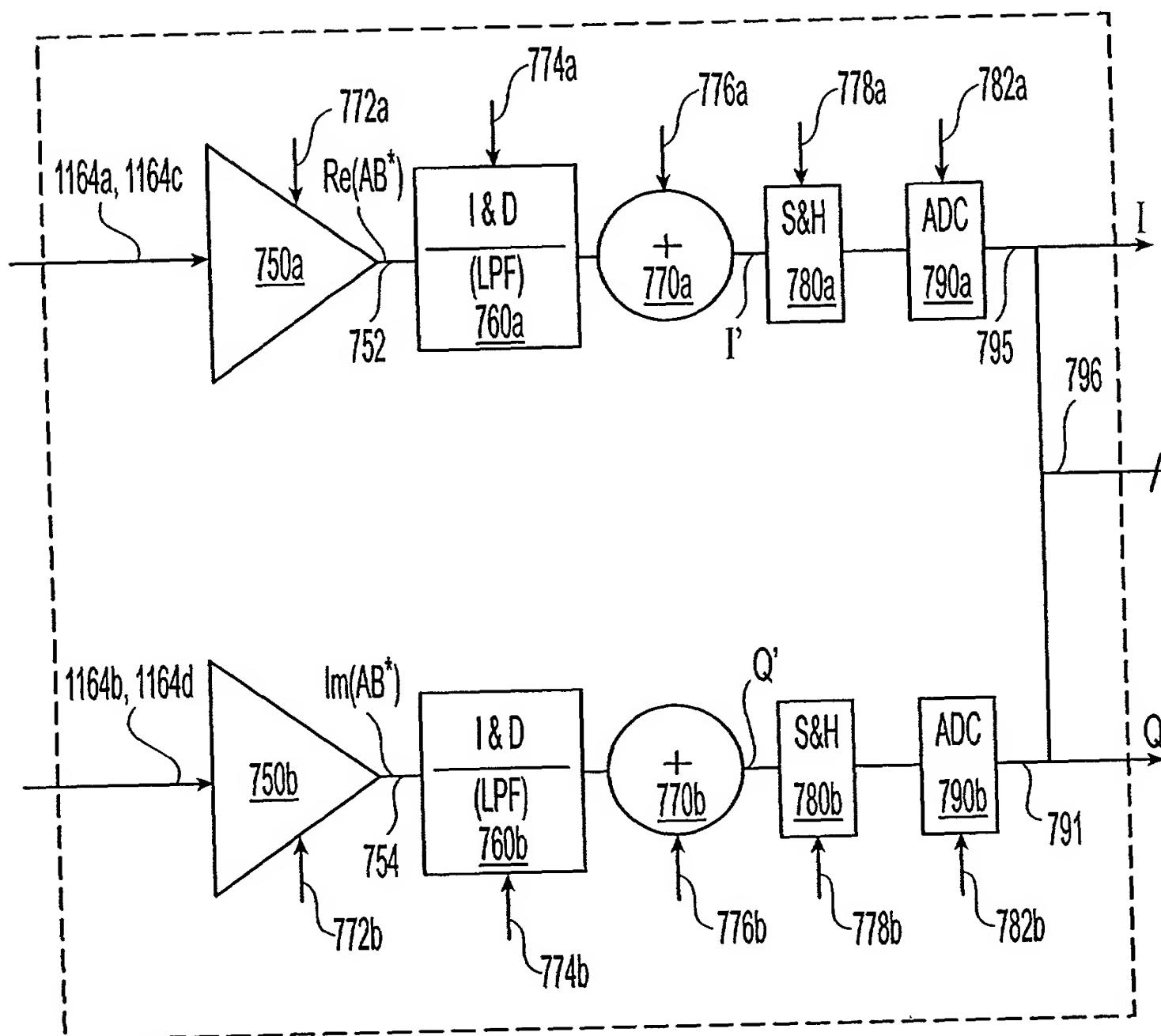


Fig. 13